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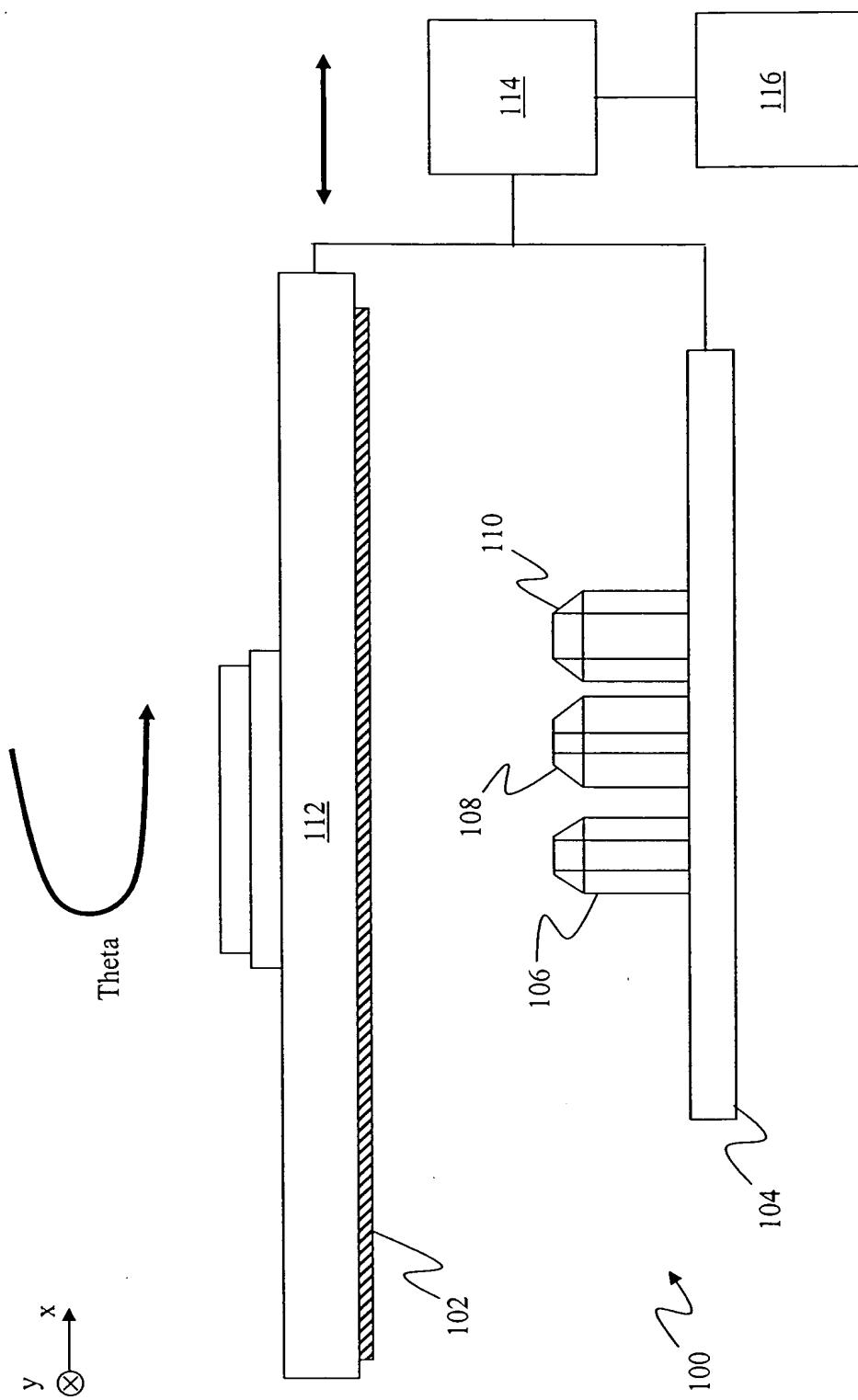


Fig. 1

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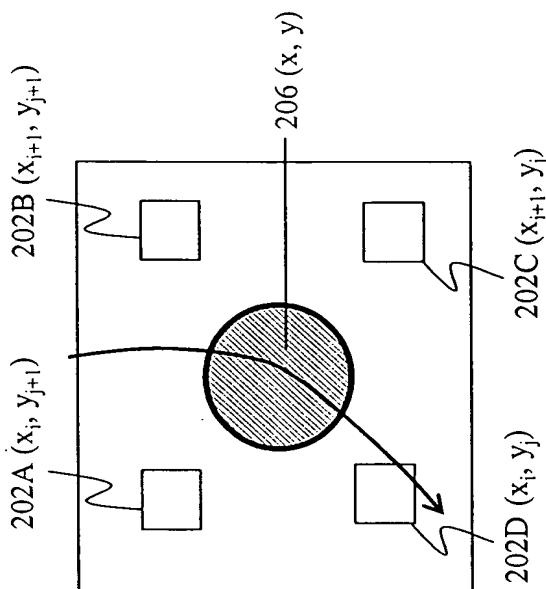


Fig. 2B

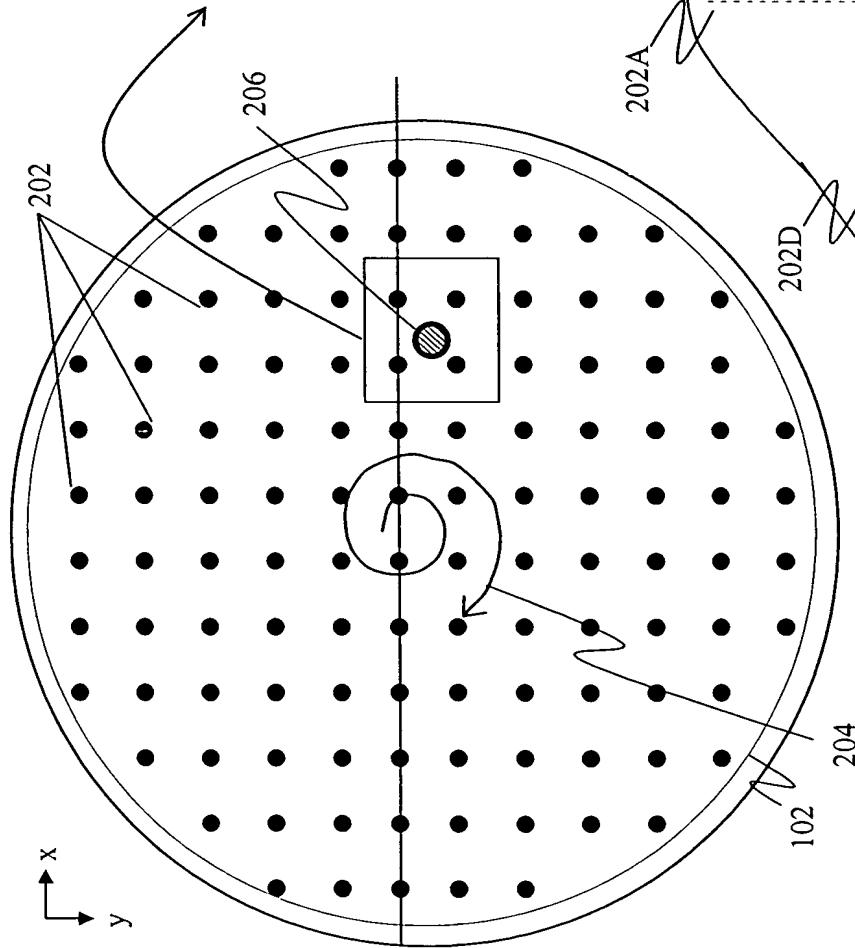


Fig. 2A

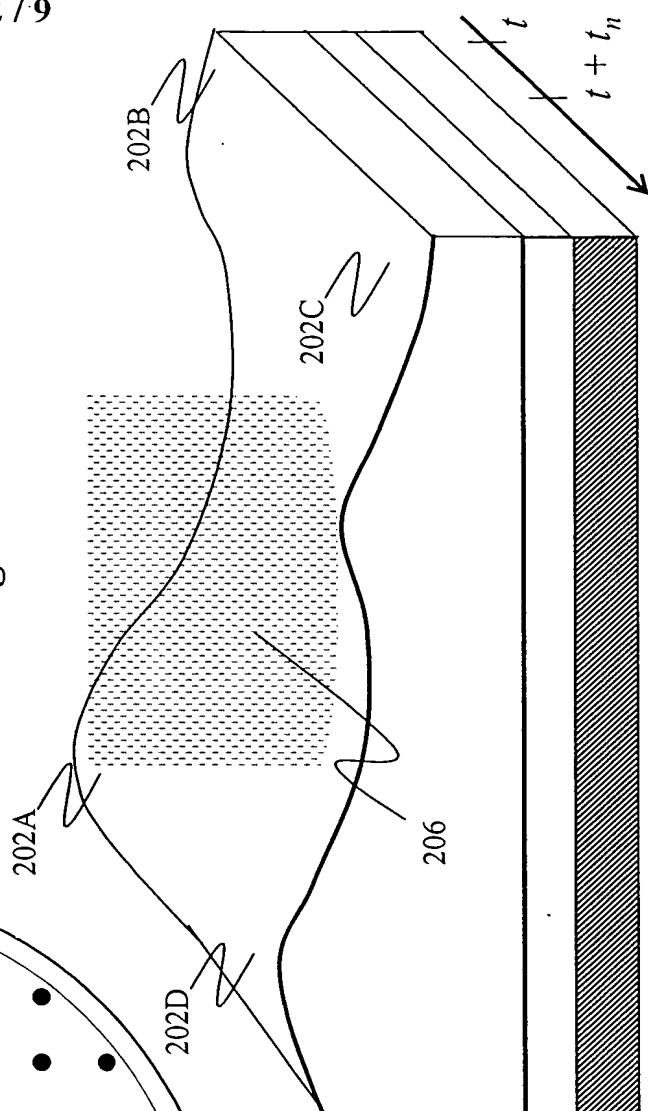


Fig. 2C

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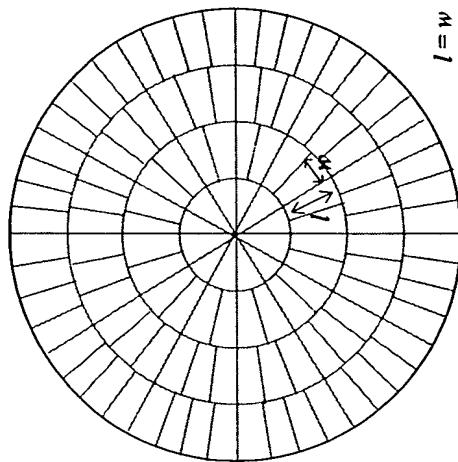
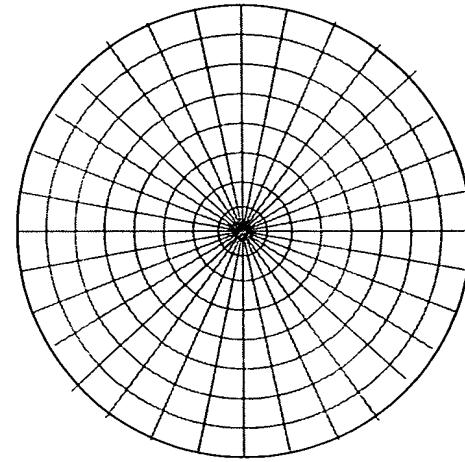
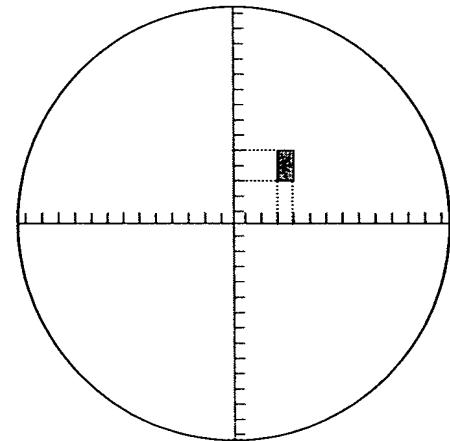
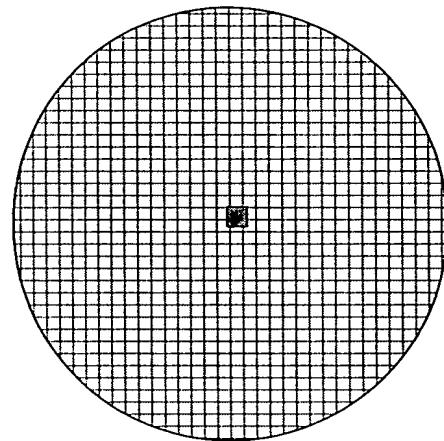
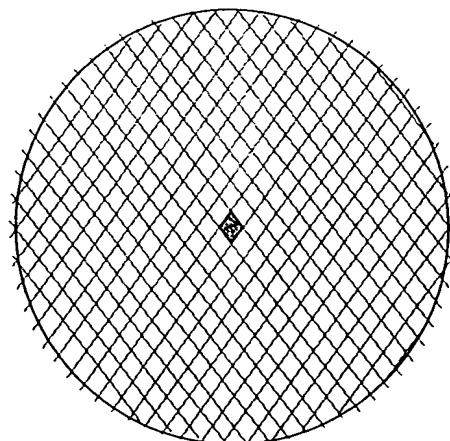
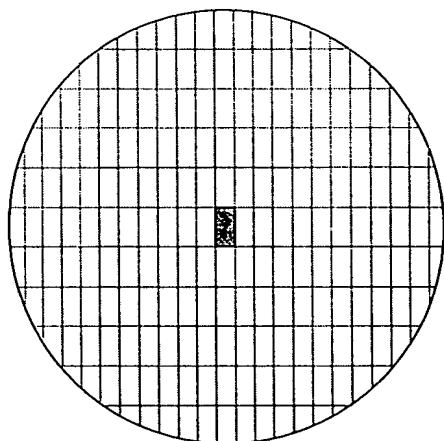
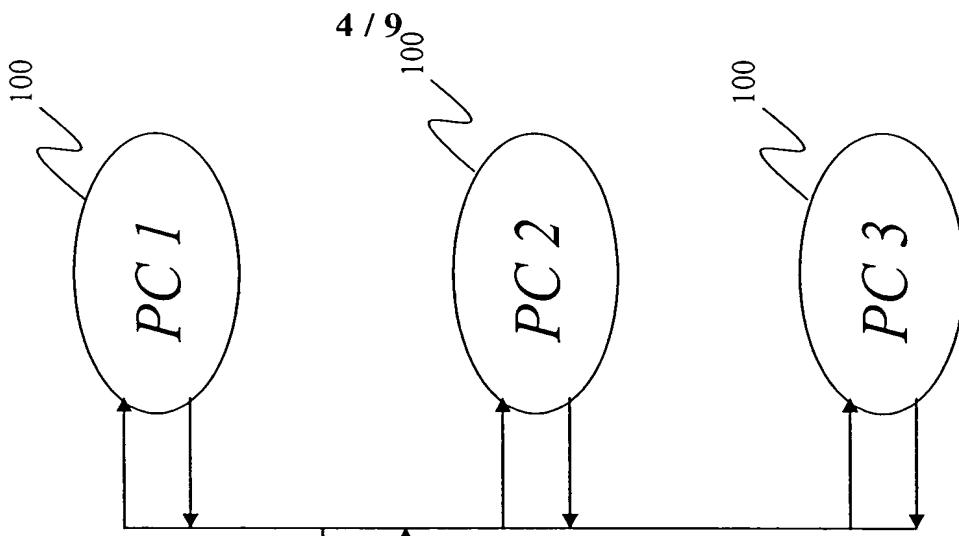
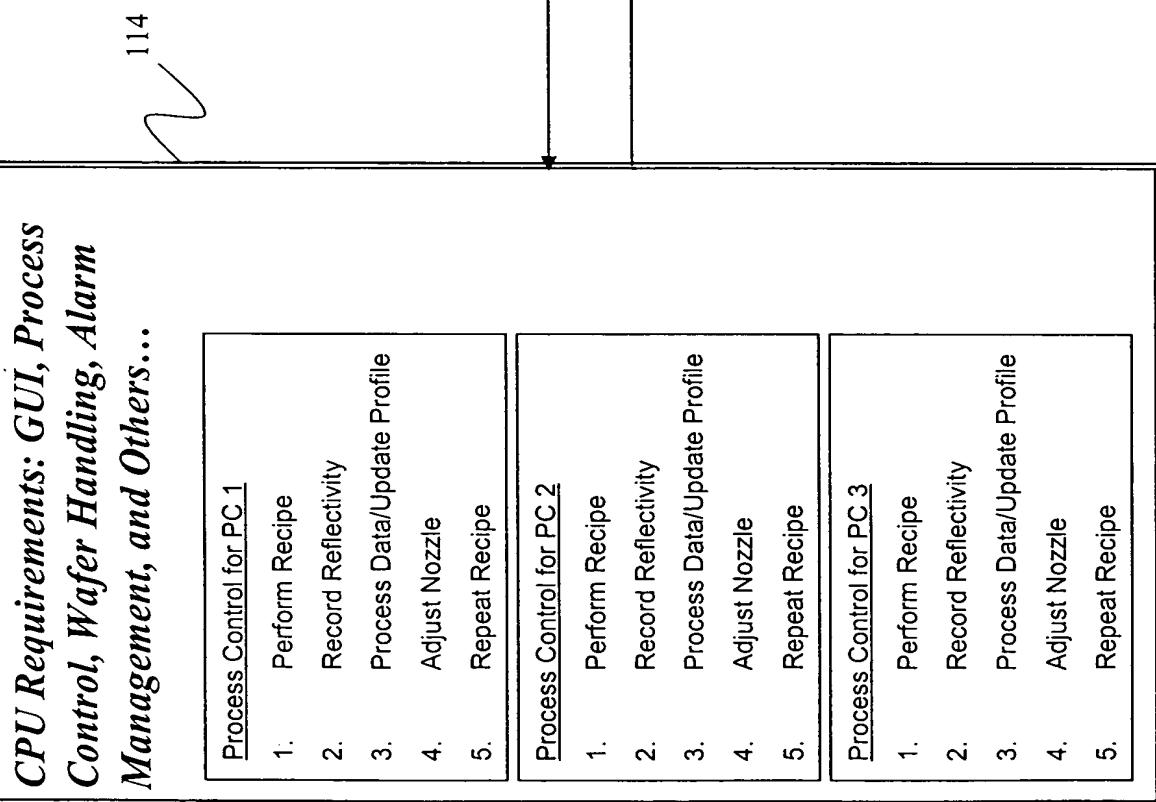
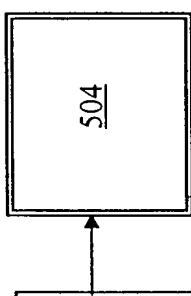


Fig. 3



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**CPU Requirements: GUI, Wafer
Handling, Alarm Management,
and Others...**



CPU: Process Control for PC 1

1. Perform Recipe
2. Record Reflectivity
3. Process Data/Update Profile
4. Adjust Nozzle
5. Repeat Recipe

502
100

PC 1

CPU: Process Control for PC 2

1. Perform Recipe
2. Record Reflectivity
3. Process Data/Update Profile
4. Adjust Nozzle
5. Repeat Recipe

502
100

PC 2

CPU: Process Control for PC 3

1. Perform Recipe
2. Record Reflectivity
3. Process Data/Update Profile
4. Adjust Nozzle
5. Repeat Recipe

502
100

PC 3

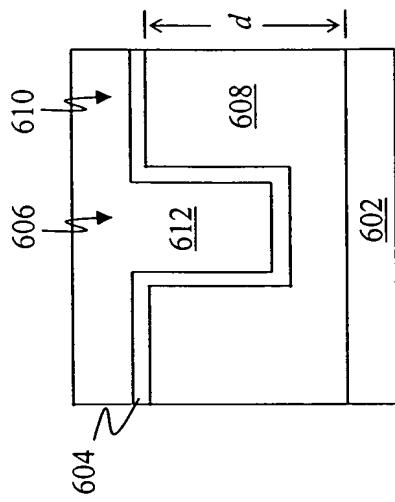


Fig. 6B

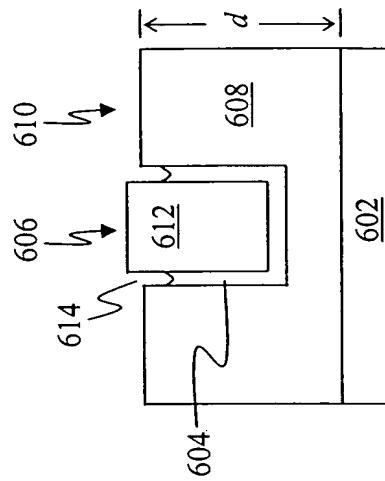


Fig. 6D

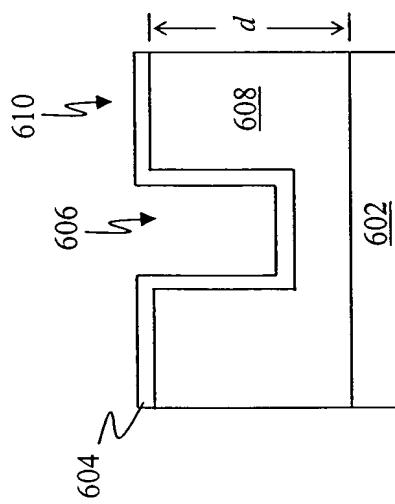


Fig. 6A

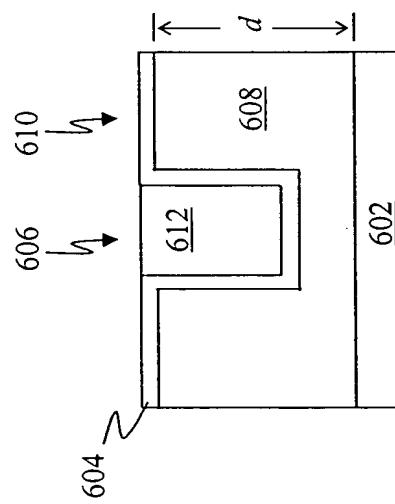


Fig. 6C

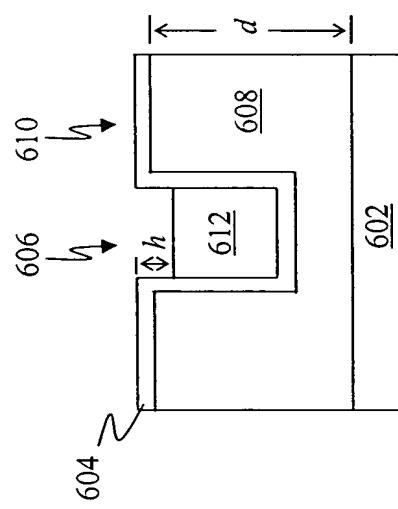


Fig. 7A

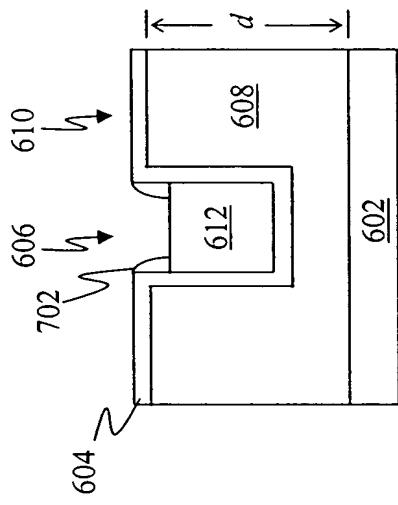


Fig. 7B

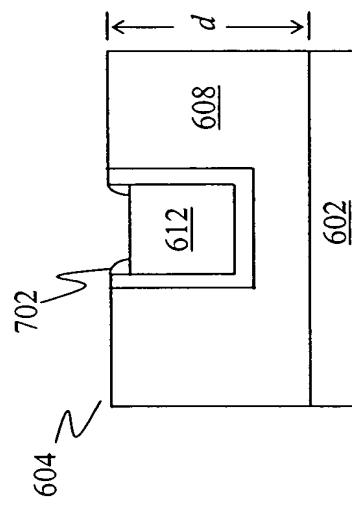


Fig. 7C

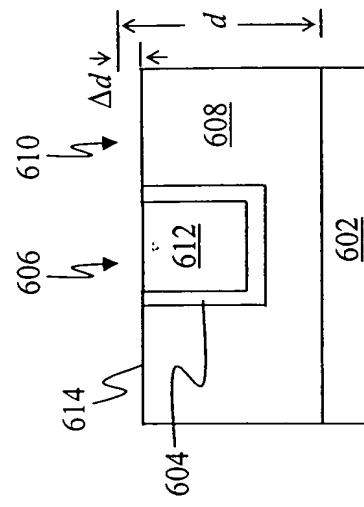


Fig. 7D

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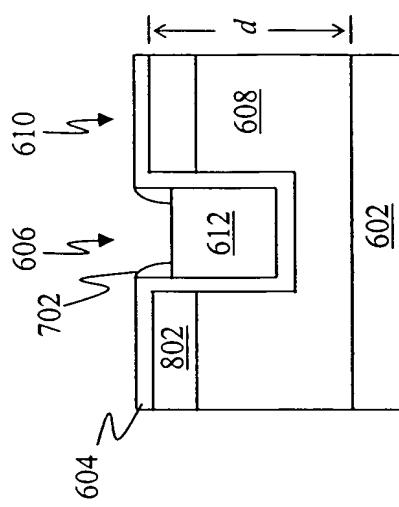


Fig. 8B

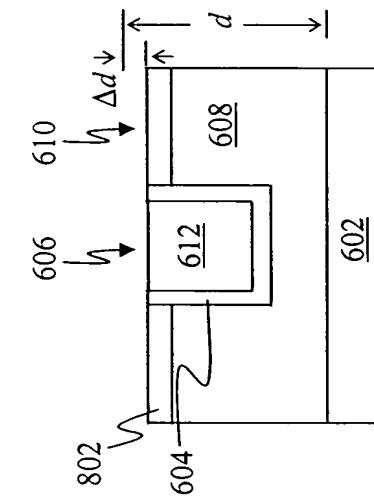


Fig. 8D

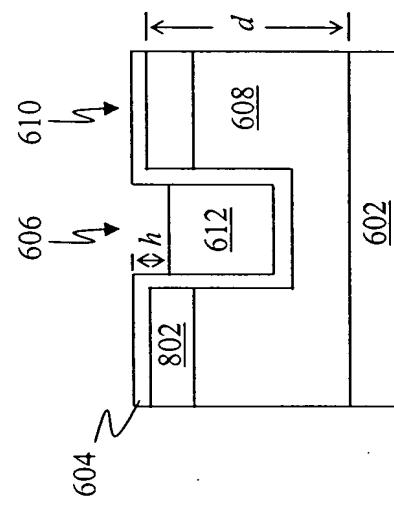


Fig. 8A

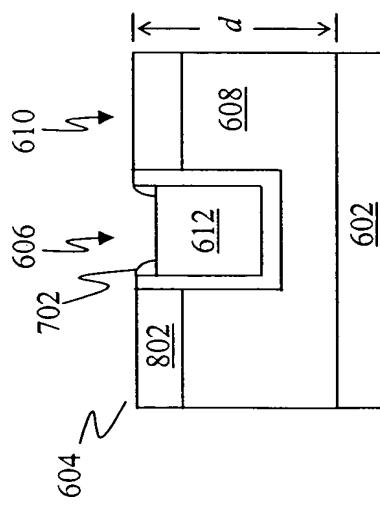


Fig. 8C

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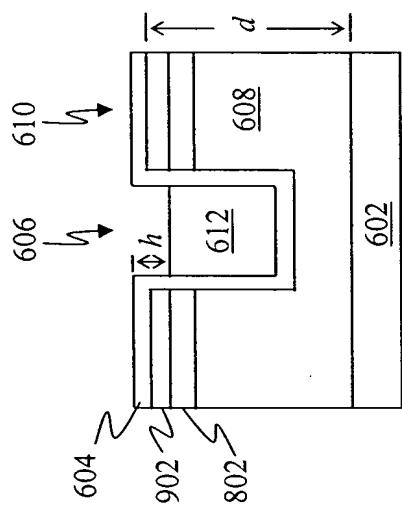


Fig. 9A

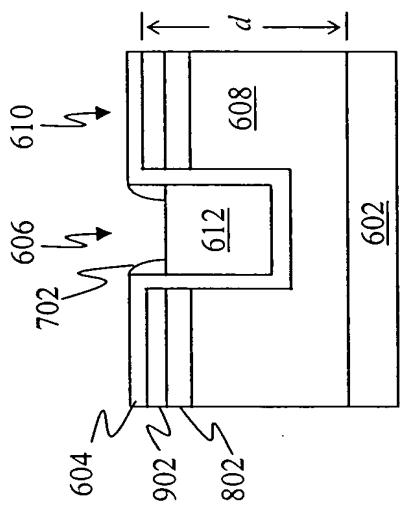


Fig. 9B

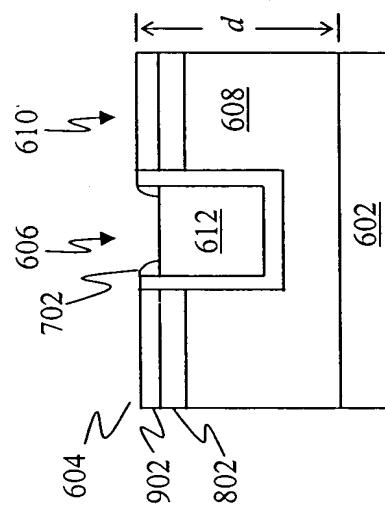


Fig. 9C

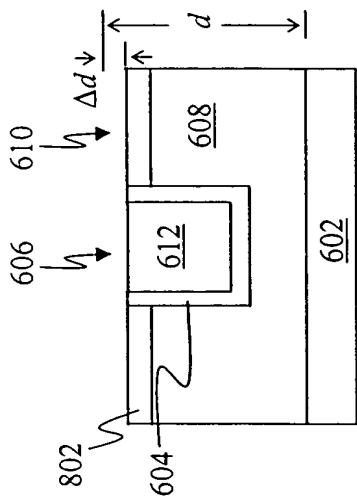


Fig. 9D